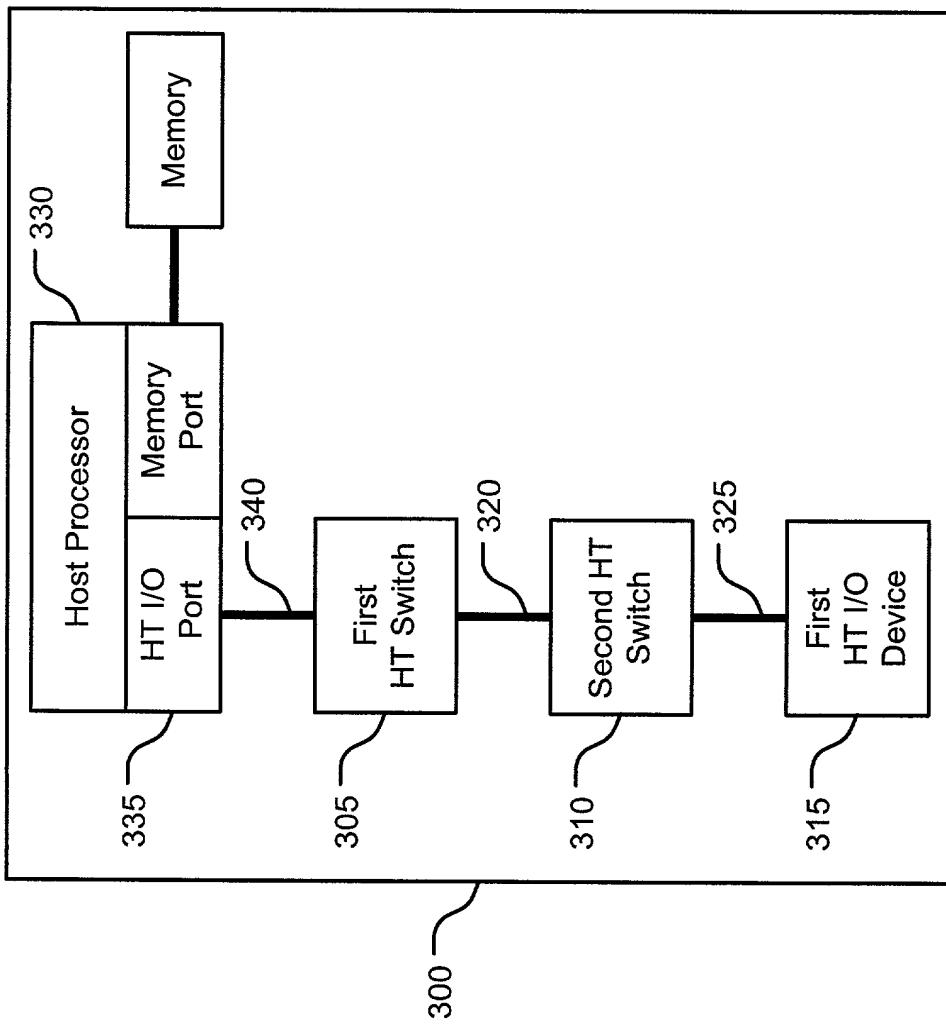


Figure 1

Command Bits				Capabilities Pointer		Capability ID	
P3 Mask	P3 Match	P2 Mask	P2 Match	P1 Mask	P1 Match	P0 Mask	P0 Match
P7 Mask	P7 Match	P6 Mask	P6 Match	P5 Mask	P5 Match	P4 Mask	P4 Match
...

Figure 2

Figure 3



400

Command Bits			Capabilities Pointer			Capability ID	
S3 Mask	S3 Match	S2 Mask	S2 Match	S1 Mask	S1 Match	S0 Mask	S0 Match
S7 Mask	S7 Match	S6 Mask	S6 Match	S5 Mask	S5 Match	S4 Mask	S4 Match

Figure 4

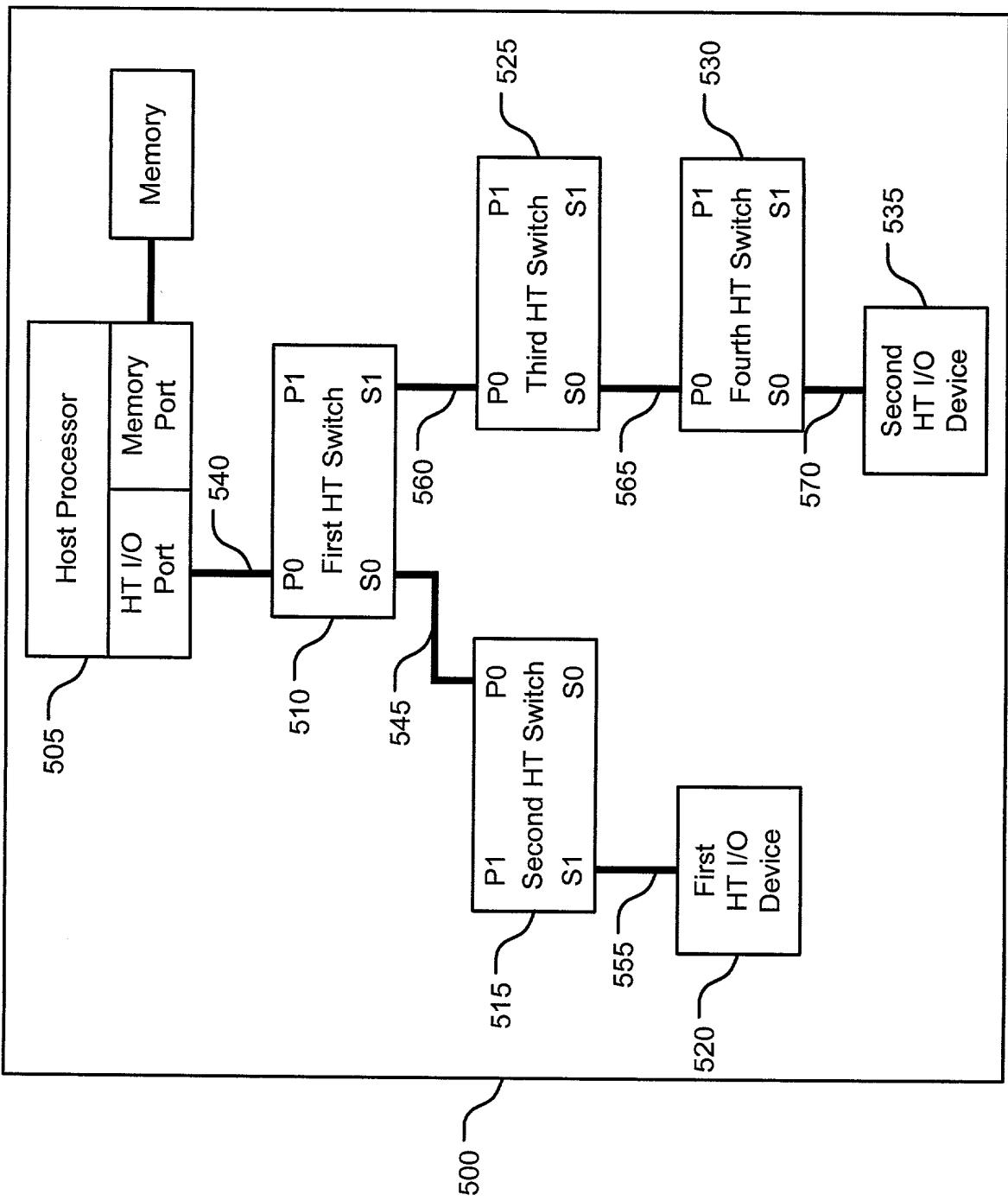


Figure 5



Command Bits			Capabilities Pointer		Capability ID		
Unit ID 3		Unit ID 2		Unit ID 1		Unit ID 0	
Secondary Segment	Primary Segment	Secondary Segment	Primary Segment	Secondary Segment	Primary Segment	Secondary Segment	Primary Segment
Unit ID 7		Unit ID 6		Unit ID 5		Unit ID 4	
Secondary Segment	Primary Segment	Secondary Segment	Primary Segment	Secondary Segment	Primary Segment	Secondary Segment	Primary Segment

Figure 6

700
705
720
725
730
735
740
745
750
755
760
765
770
775
780
785
790
795
800
805
810
815
820
825
830
835
840
845
850
855
860
865
870
875
880
885
890
895
900
905
910
915
920
925
930
935
940
945
950
955
960
965
970
975
980
985
990
995
999

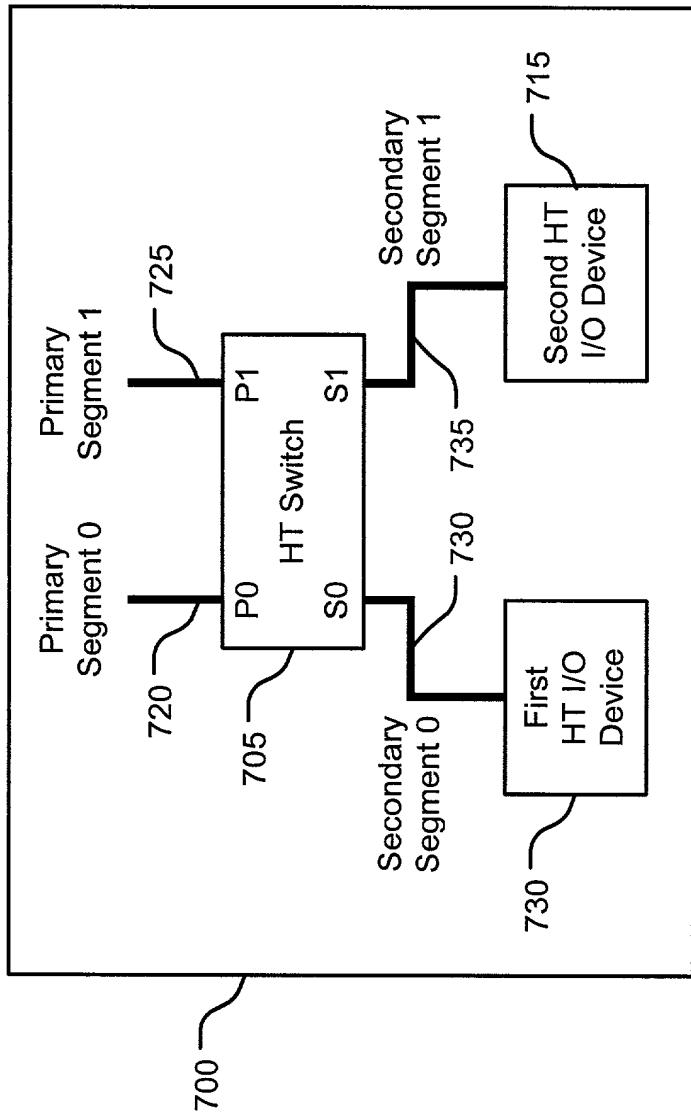


Figure 7

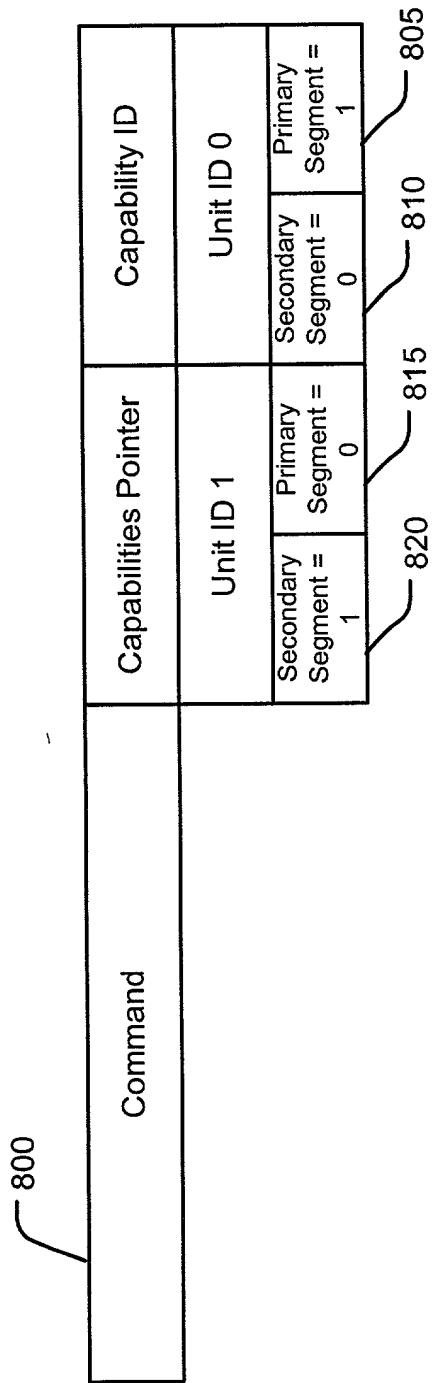


Figure 8

values: 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000 0000

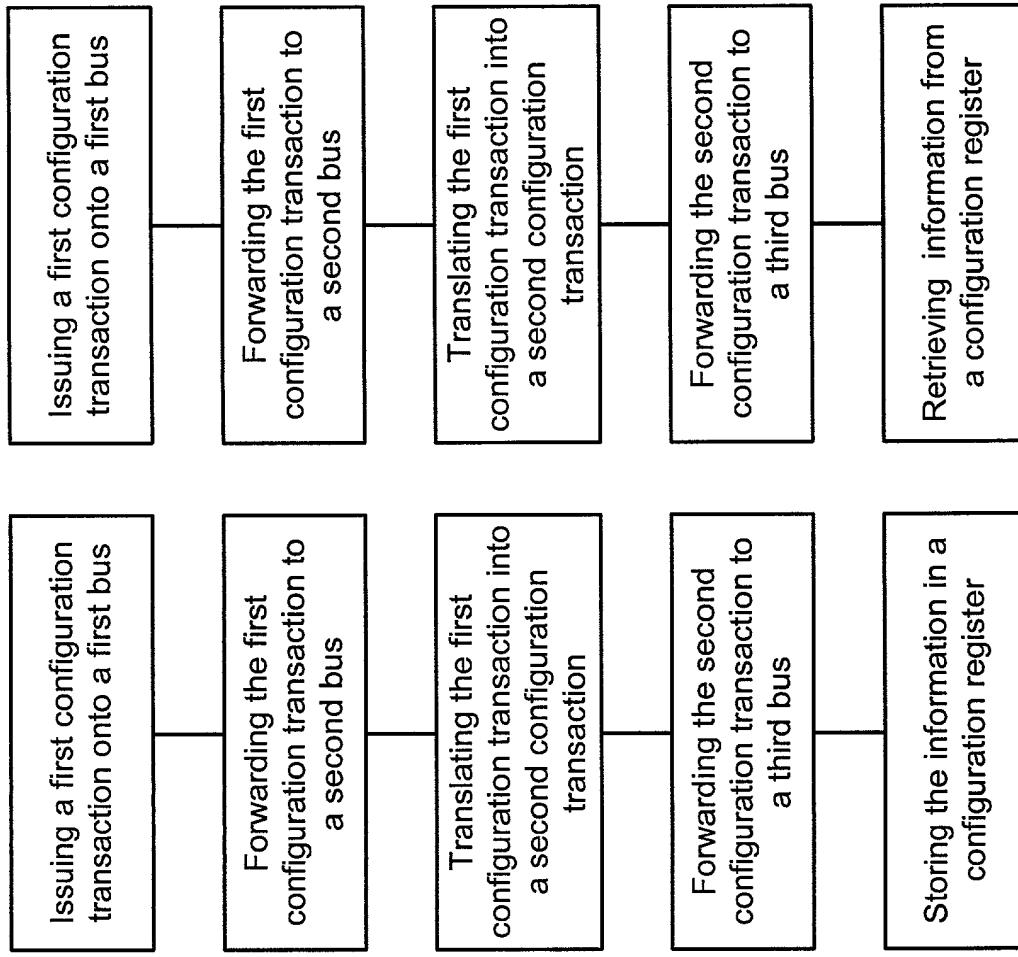


Figure 9

Figure 10

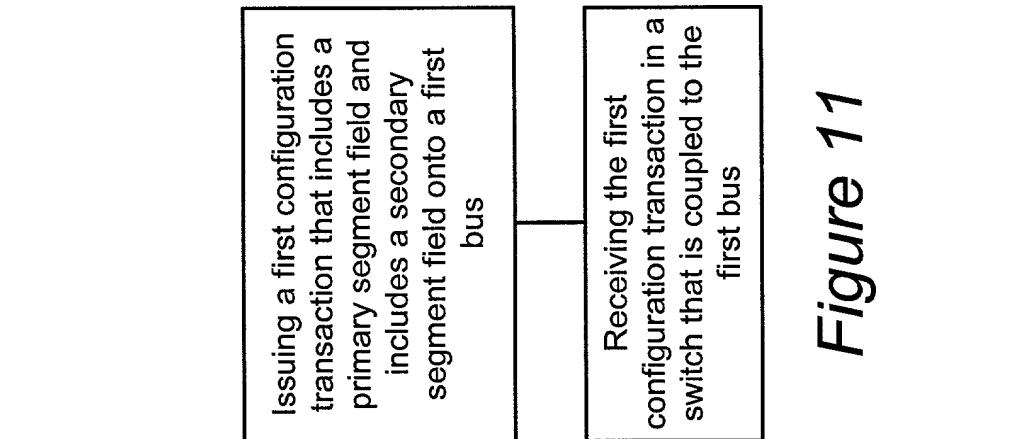


Figure 11

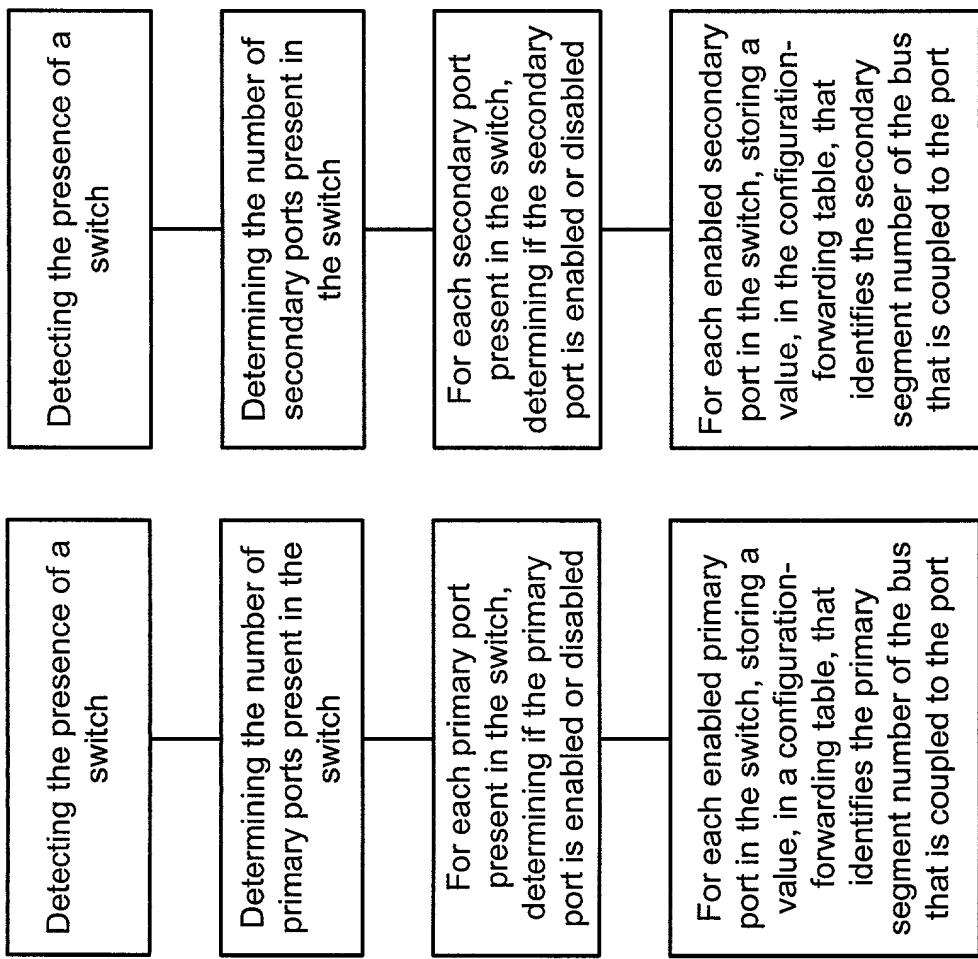


Figure 12

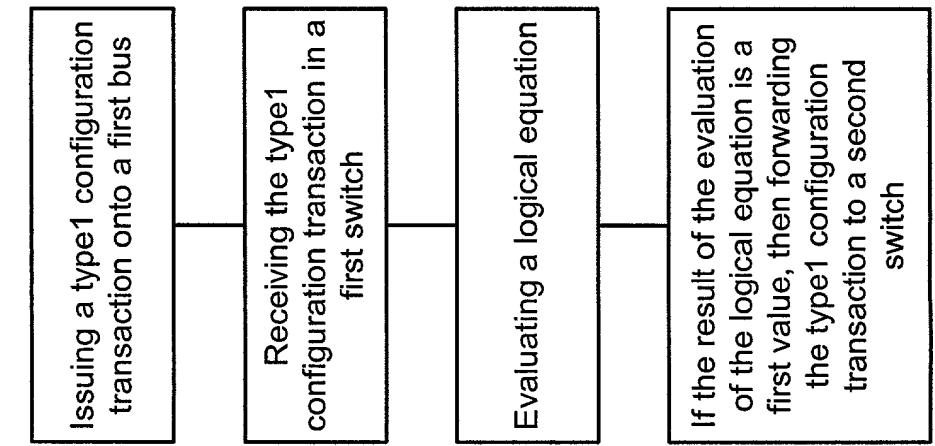


Figure 13



Figure 14

